



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of
SOWLATI ET AL

Application No. 09/545,785

Filed: April 7, 2001

For: INTERDIGITATED MULTILAYER CAPACITOR STRUCTURE FOR DEEP SUB-MICRON CMOS

Atty. Docket
US 000099

Group Art Unit: 2811

Examiner: O. Nadav

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Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT and RESPONSE under 37 C.F.R. § 1.111

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FJONES
7-9-01

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231

On: June 21, 2001
By: Elisse DeLucy

Sir:

In response to the Office action dated March 23, 2001,
please amend the above referenced application as follows and
reconsider the application in light of the following remarks.

In the Claims:

Per 37 CFR § 1.121(c)(1)(i), please rewrite claims 1, 5,

Subt B and 6 as follows.

1. (Amended) A capacitor comprising:

a first level of at least four electrically conductive

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